2018-00555 - [FACTAS] Internship Engineering / Migration and implementation of a power management and optimization algorithm on an Arduino-based prototype.

Contract type: Internship agreement
Level of qualifications required: Master's or equivalent
Function: Internship Engineering

About the research centre or Inria department
The Inria Sophia Antipolis - Méditerranée center counts 37 research teams and 9 support departments. The center's staff (about 600 people including 400 Inria employees) is composed of scientists of different nationalities (250 foreigners of 50 nationalities), engineers, technicians and administrators. 1/3 of the staff are civil servants, the others are contractual. The majority of the research teams at the center are located in Sophia Antipolis and Nice in the Alpes-Maritimes. Six teams are based in Montpellier and a team is hosted by the computer science department of the University of Bologna in Italy. The Center is a member of the University and Institution Community (ComUE) "Université Côte d'Azur (UCA)".

Context
Within the framework of a partnership between FACTAS-Inria and the startup SKAVENJI, an Arduino based prototype dedicated to power management will be implemented.

Skavenji is an emerging startup based in Nice founded in 2017, and aims to bring easy, free and Do It Yourself (DIY) energy production to the public as part of the transition to a more sustainable development. It is developing a connected device for enabling ultra-localized energy production at the room scale, with the goal of empowering citizens to imagine, make and use their own personal energy production at home. http://www.skavenji.fr/

The power system designed by Skavenji is simple (power sources and consumers, a battery), but the unpredictability of production and consumption profiles does require optimised source and battery management in order to:
- Maintain hardware simplicity and frugality,
- Minimize battery storage size,
- Maximize the power gain / use rate ("taux d'autoconsommation")

The Inria APICS research team is working on the modeling and optimisation potential of these power management issues raised by the Skavenji project. https://team.inria.fr/apics/

The intern will be earning french minimum wage (SMIC) pay.
Is regular travel foreseen for this post? No

Assignment
The objective of the internship is to transpose to an Arduino based prototype the power management and optimization algorithm designed by the research team FACTAS-Inria. The prototype will then be tested in real use-case situations and the algorithm’s behavior performance (battery management, power loss avoidance) assessed. Algorithm updates and hardware improvements will be jointly devised and implemented by the Inria-Skavenji team.

Main activities
1st - 2nd month:
- Transposing the algorithm and implementing the code.
- Identifying and documenting the use cases.

3rd - 4th month:
- Test campaign, performance assessments.

5-6th month:
- Software and hardware updates if necessary.
- Finalizing documentation.

General Information
- Theme/Domain: Optimization and control of dynamic systems
- Town/city: Sophia Antipolis
- Inria Center: CRI Sophia Antipolis - Méditerranée
- Starting date: 2018-05-01
- Duration of contract: 6 months
- Deadline to apply: 2018-05-13

Contacts
- Inria Team: FACTAS
- Recruiter: Olivi Martine / martine.olivi@inria.fr

About Inria
Inria, the French National Institute for computer science and applied mathematics, promotes "scientific excellence for technology transfer and society". Graduates from the world’s top universities, Inria’s 2,700 employees rise to the challenges of digital sciences. With its open, agile model, Inria is able to explore original approaches with its partners in industry and academia and provide an efficient response to the multidisciplinary and application challenges of the digital transformation. Inria is the source of many innovations that add value and create jobs.

Conditions for application
Defence Security:
This position is likely to be situated in a restricted area (ZRR), as defined in Decree No. 2011-1425 relating to the protection of national scientific and technical potential (PPST). Authorisation to enter an area is granted by the director of the unit, following a favourable Ministerial decision, as defined in the decree of 3 July 2012 relating to the PPST. An unfavourable Ministerial decision in respect of a position situated in a ZRR would result in the cancellation of the appointment.

Recruitment Policy:
As part of its diversity policy, all Inria positions are accessible to people with disabilities.

Warning: you must enter your e-mail address in order to save your application to Inria. Applications must be submitted
Skills
Basic electrical power notions, previous Arduino or Open Hardware / microcontroller programming experience.

Benefits package
- Subsidised catering service
- Partially-reimbursed public transport
- Social security
- Paid leave
- Flexible working hours
- Sports facilities